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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/055,445	1/23/2002	David J. Potts	TI-27832	6368

TITLE OF INVENTION: SEMICONDUCTOR WAFER WITH GROUPED INTEGRATED CIRCUIT DIE HAVING INTER-DIE CONNECTIONS FOR GROUP TESTING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	12/16/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
Geyer, Scott B.	2829	438-110000

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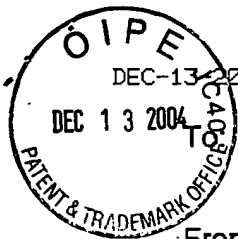
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Texas Instruments Incorporated
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of
First Inventor: Potts

Docket Number: TI-27832

Serial No.: 10/055,445

Art Unit: 2829

Filed: 01/23/02

Examiner: Geyer, Scott B.

For: SEMICONDUCTOR WAFER WITH GROUPED INTEGRATED CIRCUIT DIE
HAVING INTER-DIE CONNECTIONS FOR GROUP TESTING

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NAME OF INVENTOR(S): Potts	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/055,445	
TITLE OF INVENTION: SEMICONDUCTOR WAFER WITH GROUPED INTEGRATED CIRCUIT DIE HAVING INTER-...	
Filing Date: 01/23/2002	
TI FILE NO.: TI-27832	DEPOSIT ACCT. NO.: 20-0668
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